FIG.1

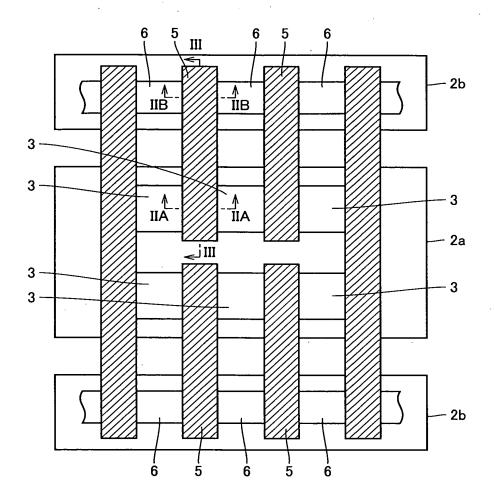


FIG.2A

FIG.2B

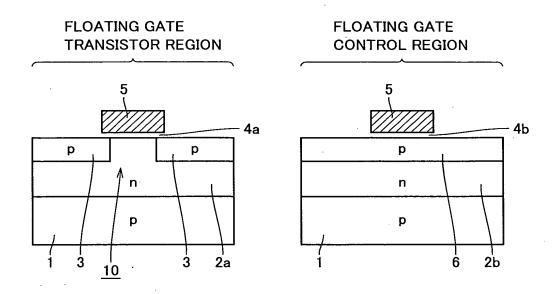


FIG.3

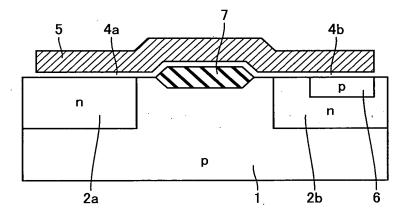


FIG.4

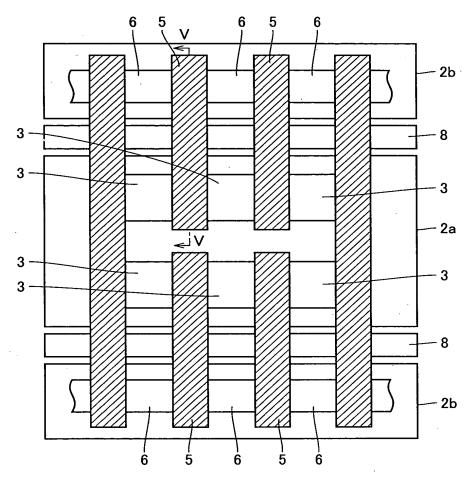


FIG.5

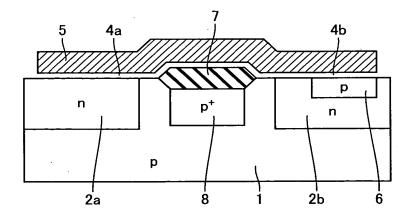


FIG.6

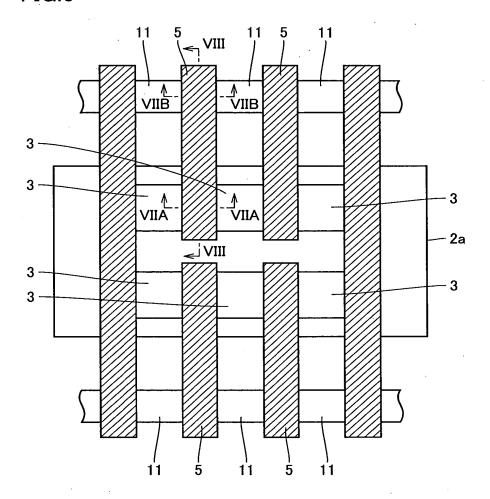


FIG.7B

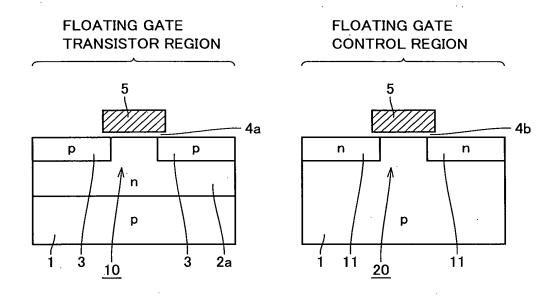


FIG.8

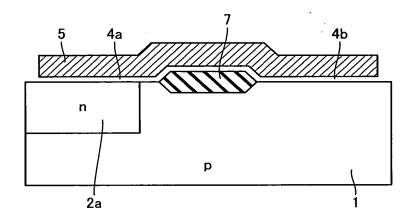


FIG.9

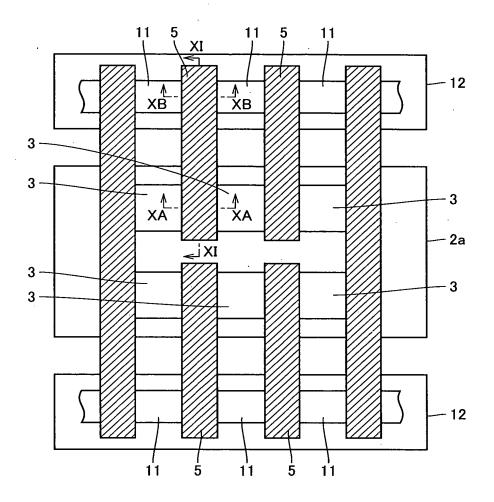


FIG.10B

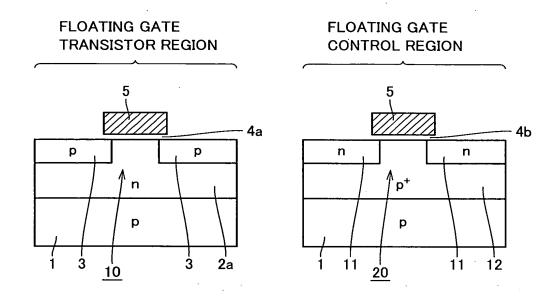


FIG.11

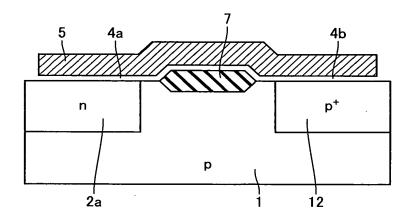


FIG.12

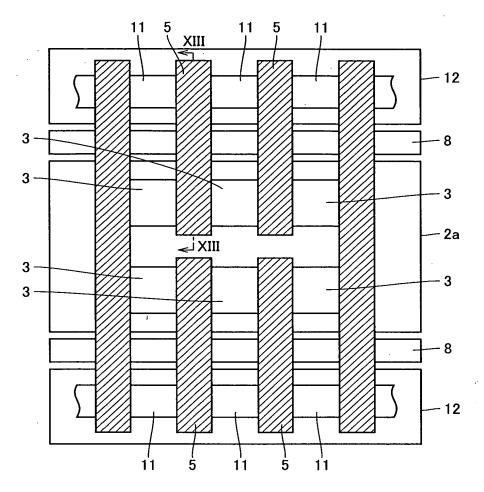


FIG.13

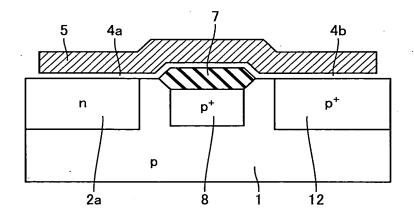


FIG.14

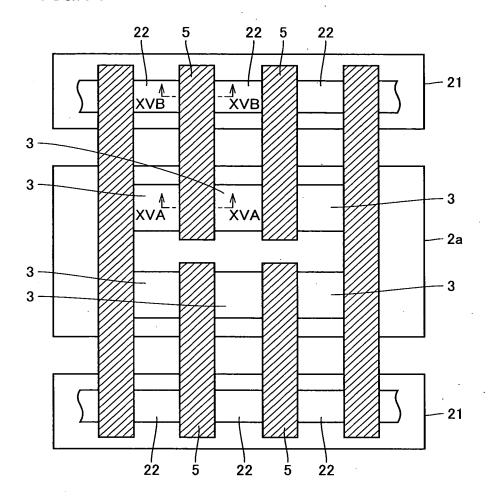


FIG.15B

FLOATING GATE **FLOATING GATE** TRANSISTOR REGION **CONTROL REGION** - 4a 4b р р р р n р 22 22 3 21 <u>10</u> <u>30</u>

FIG.16

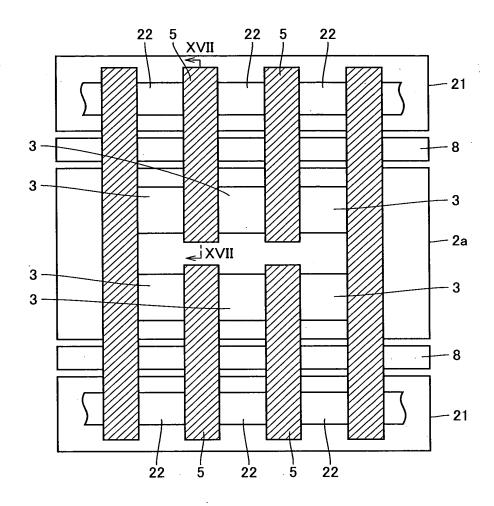


FIG.17

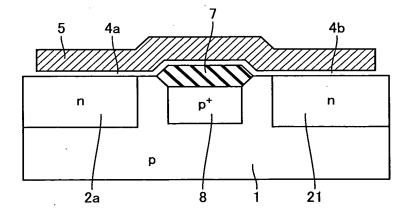


FIG.18

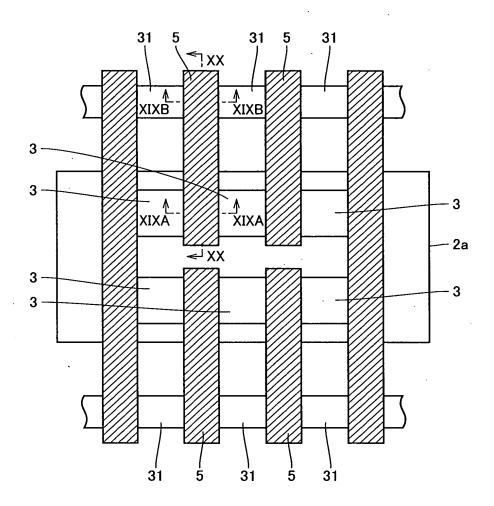


FIG.19B

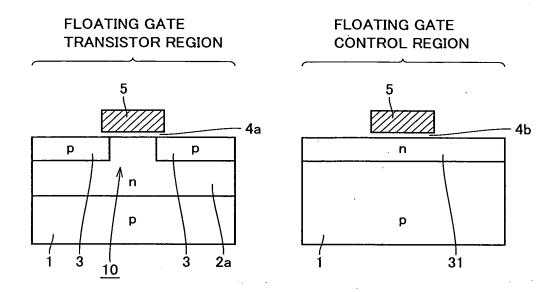


FIG.20

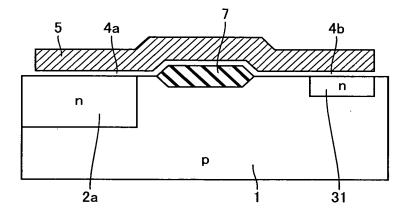


FIG.21

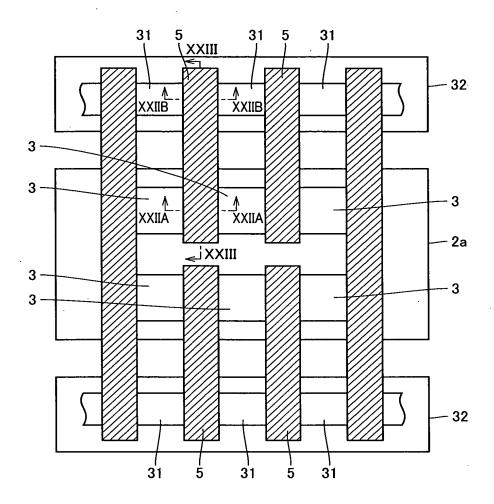


FIG.22B

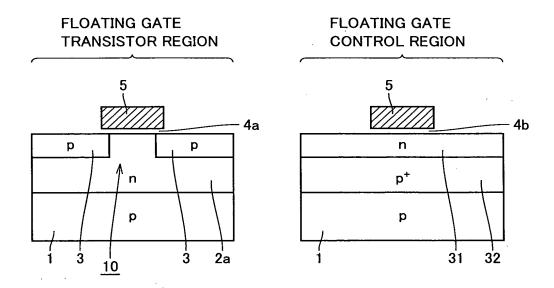


FIG.23

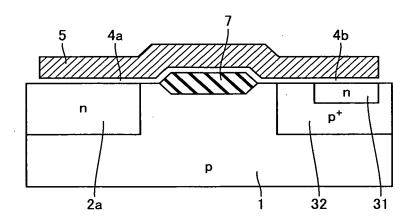


FIG.24

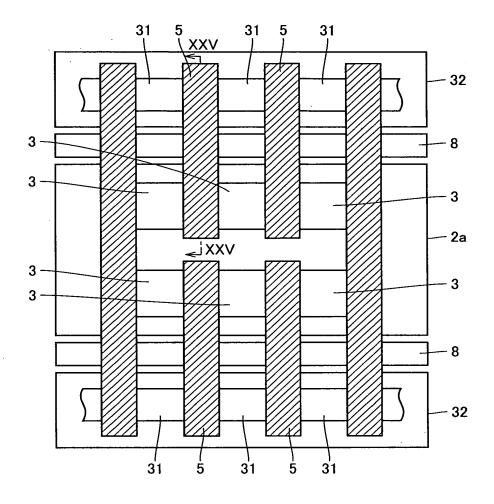


FIG.25

